

Response
Application No. 09/991,749
Attorney Docket No. 011503

REMARKS

Claims 1 - 10 are pending in the present application. By this Amendment, claims 1 and 9 have each been amended to include the features of claim 4, which has been cancelled. No new matter has been added. It is respectfully submitted that this Amendment is fully responsive to the Office Action dated May 5, 2006.

As to the Merits:

The Examiner sets forth the following rejections:

claims 1-2 and 5-10 were rejected as being unpatentable over Yanai (JP 05-046121 U) in view of Shiga et al. (US 6,240,019 B1 hereinafter "Shiga");

claim 3 was rejected as being unpatentable over Yanai in view of Shiga, and further in view of Yanagibori (US 4,919,640);

claim 4 was rejected as being unpatentable over Yanai in view of Shiga, and further in view of Ogita (US 4,225,823); and

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claims 1 and 9 were rejected as being unpatentable over Yanai in view of Ryu (JP 06-203584 A).

Applicants traverse each of these rejections.

It is submitted that none of Yanai, Shiga, Yanagibori, Ogita and Ryu discloses a capacitor coupled to the zener diode for smoothing the clamped voltage to generate a boosted voltage, as recited in claims 1 and 9.

Yanai discloses a radio receiver that includes a DC-DC converter (booster circuit) 13 for providing the VCO 7 with a control voltage via the low-pass filter 12.

Shiga discloses a booster circuit 7 for providing the drain of a cell transistor with a drain voltage.

Yanagibori discloses a detecting circuit 16 that performs AM-detection from an amplified intermediate frequency signal of the intermediate frequency amplifying circuit 15.

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Ogita discloses a high frequency amplifier 85 that includes a transfer switch 81, a diode 89, and a booster amplifier 79 including a MOS transistor 75, a coil 76, a capacitor 77, and a register 78 (see Fig. 5).

Ryu discloses a flash memory device that includes a DC-DC converter (booster circuit) 36 for providing an EEPROM 36 with a high voltage necessary for data writing or erasing.

The capacitor 77 of Ogita is used as a filter for filtering an input signal supplied from an antenna but not used to smooth the clamped voltage generated by the diode 89. Since the capacitor of the present invention smoothes the clamped voltage to generate a boosted voltage, it is possible to use the booster circuit as a circuit for providing a boosted voltage and a circuit for providing a write voltage of a non-volatile memory.

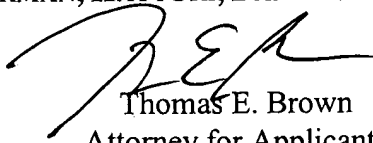
In view of the aforementioned amendments and accompanying remarks, Applicants submit that the claims, as herein amended, are in condition for allowance. Applicants request such action at an early date.

If the Examiner believes that this application is not now in condition for allowance, the Examiner is requested to contact Applicants' undersigned attorney to arrange for an interview to expedite the disposition of this case.

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If this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,
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